

08/29/2002

15:47

LSI LOGIC-IP DEPT./4084337460 → 917038729318

NO.815

0002

8/Election
+ A not A.
9.3.02
Moussi

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: John de Q. Walker, et
al.

) Group Art Unit: 2811

) Examiner: Ori Nadav

Serial No.: 10/035,346

) Atty. Docket No.: 01-152

Filed: December 28, 2001

For: CMOS varactor with constant dC/dV
characteristic

FAX COPY RECEIVED

RESPONSE TO OFFICIAL ACTION
Restriction/Election Requirement

AUG 29 2002

TECHNOLOGY CENTER 2800

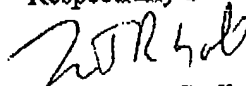
Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

This response is presented to the Office Action mailed August 20, 2002, wherein the Examiner required restriction pursuant to 35 U.S.C. §121. Election is hereby made, without traverse, to prosecute Group I, claims 1-10.

Please withdraw claims 11- 17 (i.e., Group II) from consideration.

Respectfully Submitted,



Timothy R. Croll

Reg. No. 36,771

(408) 433-7625

LSI LOGIC CORPORATION

1551 McCarthy Blvd.

M/S D-106

Milpitas, CA 95035-7451

Date: August 28, 2002